COMPUTER SCIENCE &ENGINEERING

NEWSLETTER

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PHD STUDENT KENNETH O'NEAL INTERNSHIP EXPERIENCE

Your first internship can be both frightening and exciting, thrusting you into a whirlwind of preparing to relocate, fathoming the seemingly opaque nature of what's to come, and the excitement about your first, degree relevant, position. It can be nearly overwhelming. However, after arriving, meeting the team, and being put in contact with your mentors, the fear of the unknown subsides, and the scope of the opportunity afforded by an internship is made clear. You've been given a trial run, a 90-Day free return policy normally reserved for purchasing a mattress. It's an opportunity to test if all the challenging work that have brought you to this point, and the work and research you will pursue in your career is leading you down an avenue you will enjoy!

Thankfully my repeat internships, focusing on pre-silicon design, simulation and performance estimation of architectures have been pleasurable. In all 4 internships, the people are welcoming and the work equal parts engaging, difficult and motivating. Further, I've managed to culminate working relationships with passionate engineers and researchers who actively support my academic ventures and career aspirations as well as who advocate for and guide me through the process of publishing industry research in academic venues on two separate occasions; my DAC 2017 best paper nominee and my CODES+ISSS 2017 paper. In short, an internship and the people you meet during your employment will guide you on a process of self-discovery, scientific maturation, and greater career awareness, especially regarding the familiar yet altogether different competitive environment industry driven research is. While Intel Corporation, my soon to be employer might not be for everyone, an Internship is rife with opportunity to expand your horizons, network with capable and senior researchers, and really test your mettle, all before exercising your 90-day return policy.

DISTINGUISHED LECTURE SERIES

Moti Yung
Snapchat, Inc.
January 19, 2018
11:10am - 12:00 pm
Bourns A125

Divesh Srivastava
AT&T Research Labs
February 9, 2018
11:10am - 12:00pm
Bourns A125

Joel Emer
nVidia/Massachisetts Institute of Technology
February 23, 2018
11:10am - 12:00pm
Bourns A125

Maria Klawe
Harvey Mudd College
March 9, 2018
11:10am - 12:00pm
Bourns A125

BEST POSTER AWARD AT SIGSPATIAL 2017

A paper titled "Large Scale Analytics of Vector+Raster Big Spatial Data" from Prof. Ahmed Eldawy's lab. won the best poster presentation award at ACM SIGSPATIAL 2017, a top-tier conference for spatial data processing. The paper proposes a new method for scalable analysis of satellite data. The presentation was selected among 41 posters presented at the conference. The paper was co-authored with two former UCR students, Lyuye Niu, MS Computer Engineering, and Zhiba Su, an undergraduate exchange student from the GPP program, along with David Haynes from the University of Minnesota.

BEST PAPER AWARD AT ICDM 2017

Congrats to Ph.D. student Yan Zhu who won the best paper award at IEEE ICDM 2017 [a]. Ms. Zhu also had a paper shortlisted for this award in 2016. This is the second win at this conference for Professor Keogh's lab. In IEEE ICDM 2007, Dragomir Yankov (now at Microsoft) also won this award.

[a] Yan Zhu, Makoto Imamura, Daniel Nikovski, and Eamonn Keogh. Matrix Profile VII: Time Series Chains: A New Primitive for Time Series Data Mining. IEEE ICDM 2017

FACULTY FEATURE
PROFESSOR K.K. RAMAKRISHNAN

Professor K. K. Ramakrishnan received a $1.2M grant from the National Institute of Standards and Technology (NIST), Public Safety Innovation Accelerator Program for "Modeling and Development of Resilient Communication." He leads a team that includes PIs from University of Central Florida, University of Illinois, Chicago and Rutgers University. The project will develop resilient communication for first responders in disaster situations, which can dramatically affect outcomes. The proposed resilient architecture allows dynamically formed groups of first responder teams to communicate even when there is damage to infrastructure.

Prof. Ramakrishnan has also been elevated to the status of Fellow in the Association for Computing Machinery (ACM). He is one of 54 distinguished international researchers recognized in the 2017 ACM Fellow class. Elevation to the status of Fellow recognizes exceptional contributions to the computing field, and is a status achieved by less than 1% of the ACM membership. Ramakrishnan's citation states: "For contributions to congestion control, operating system support for networks and virtual private networks."

In other news, a recent paper he co-authored, "SAP: Stall-Aware Pacing for Improved DASH Video Experience in Cellular Networks" was awarded the first prize in the DASH Industry Forum Excellence in DASH Award at the ACM MMSys 2017 conference.

Also, another paper he co-authored, "Design and Characterization of a Full-Duplex Multiantenna System for WiFi Networks," in IEEE Transactions on Vehicular Technology, from March 2014 won the **2017 Jack Neubauer Memorial Award** recognizing the Best Systems Paper published in the IEEE Transactions on Vehicular Technology.

STAY CONNECTED

Department of Computer Science and Engineering

351 Winston Chung Hall

Riverside, CA 92521

Tel: (951) 827-5639

cs.ucr.edu

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FEATURED ALUMNI
DR. TITUS WINTERS

Titus is a Senior Staff Software Engineer at Google, where he has worked since 2010. He received a BS from Harvey Mudd College in 2002, a Master's Degree from UCR in 2004, and a Ph.D. from UCR in 2006 working with Dr. Tom Payne. Academically, his interests have ranged from systems and security as an undergrad into applied AI and CS Education as a graduate student. During his professional tenure at Google, he has focused primarily on the C++ programming language - maintaining Google's core libraries, teaching C++ and software engineering practice to Google engineers, and providing guidance for Google's 12,000+ software engineers working in C++. His teams have been at the forefront of automated large-scale code transformation, building the tools and policies necessary to make change possible at scale in a shared codebase of 250 million lines of code. He is a frequent presenter at CppCon - the world's largest C++-focused professional conference. This year he led the launch of Abseil (http://abseil.io) - an ongoing open-source release of Google's common C++ libraries. He also serves on the international standardization body for C++ where he was recently appointed to chair the Library Evolution Working Group - the subgroup responsible for direction and design for the C++ standard library. He and his wife Victoria (UCR '04) live in Queens, NY and enjoy rock climbing, travel, and the card game Hanabi.